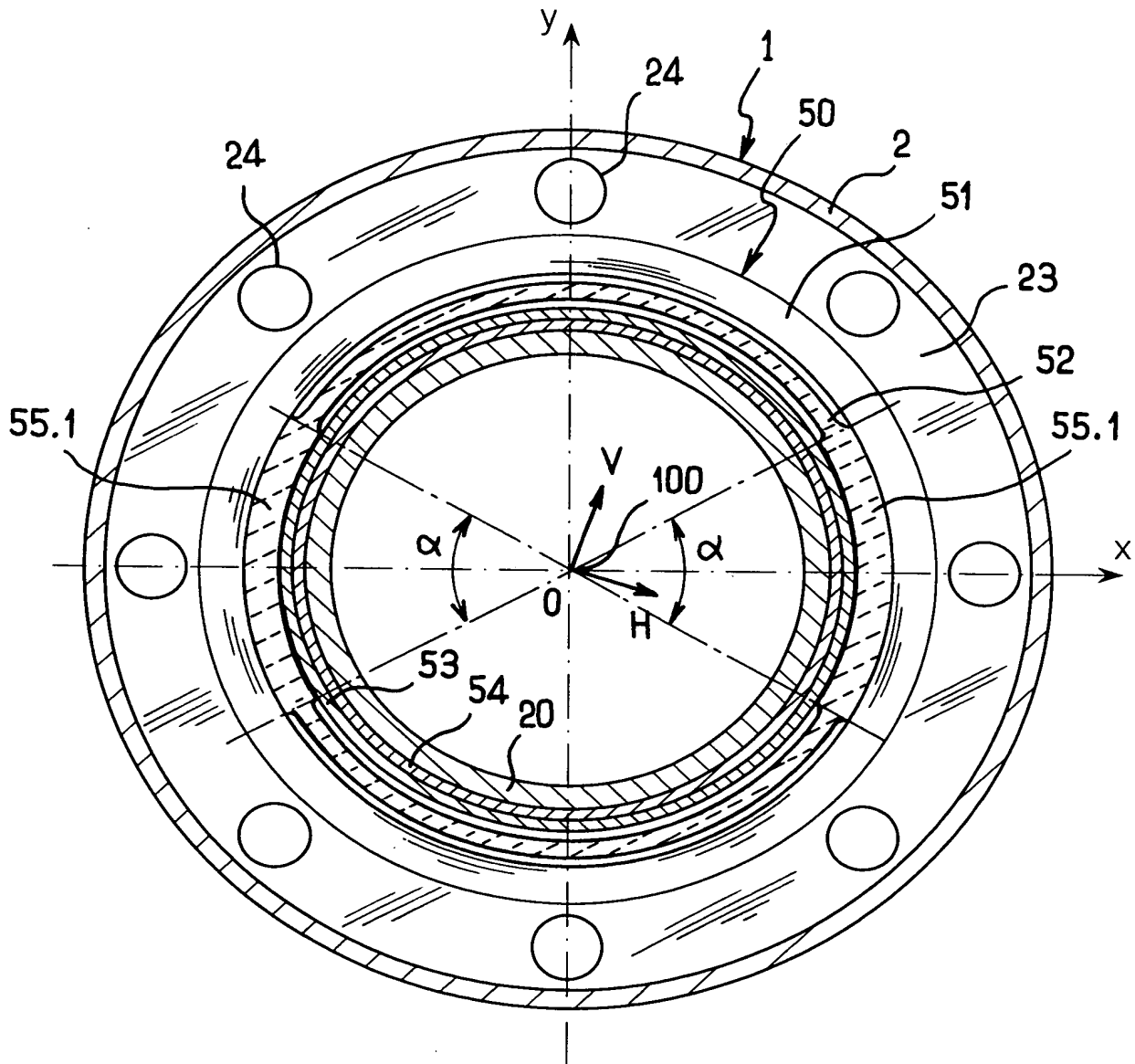




FIG. 10 is a cross-sectional view of a semiconductor device. It shows a substrate 50 with a layer 20 on top. A central region contains a stack of layers 23, 24, 51, 52, 53, and 54. A dashed circle indicates a cross-section along line X-X.

3 / 9

FIG. 3

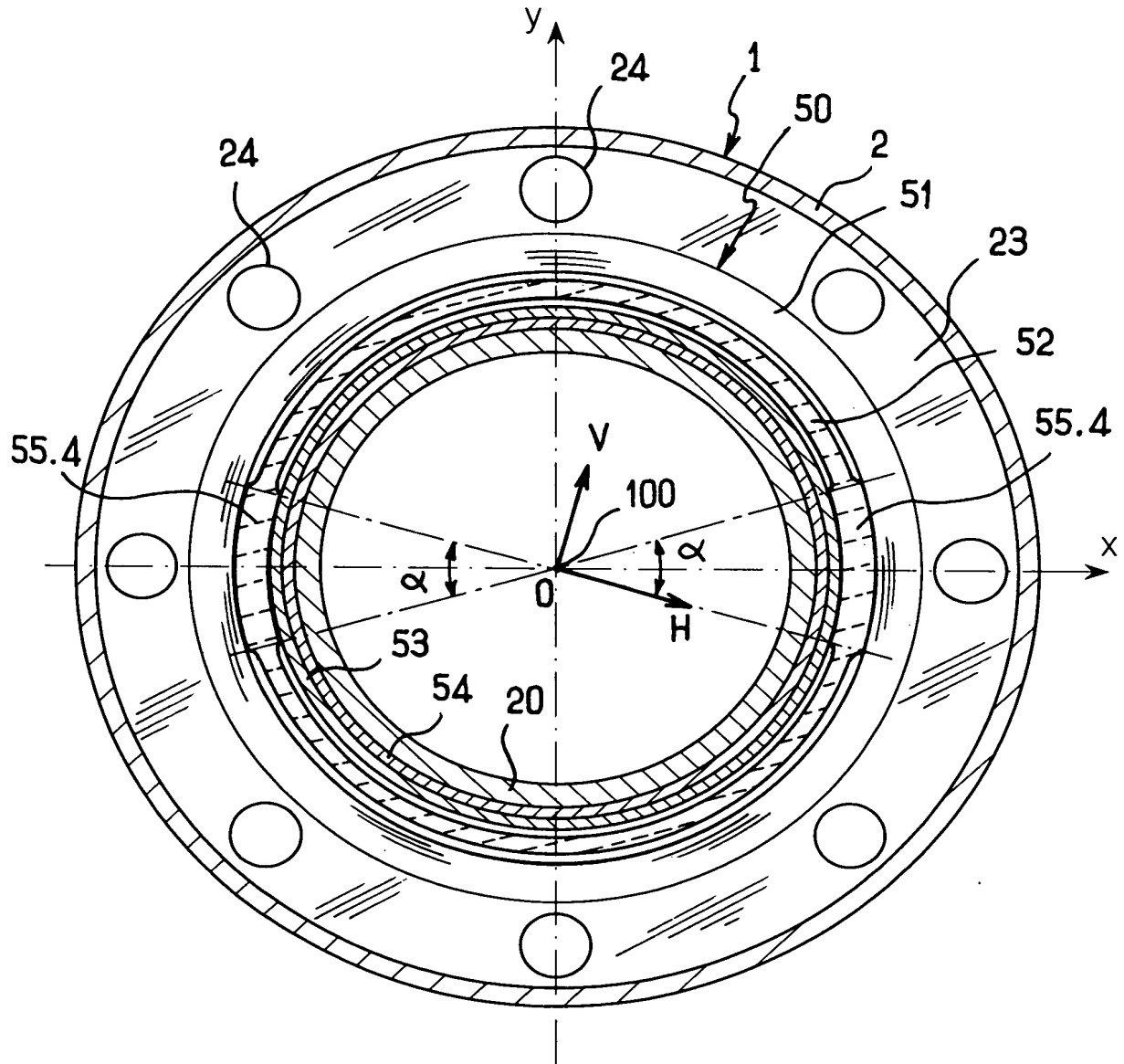


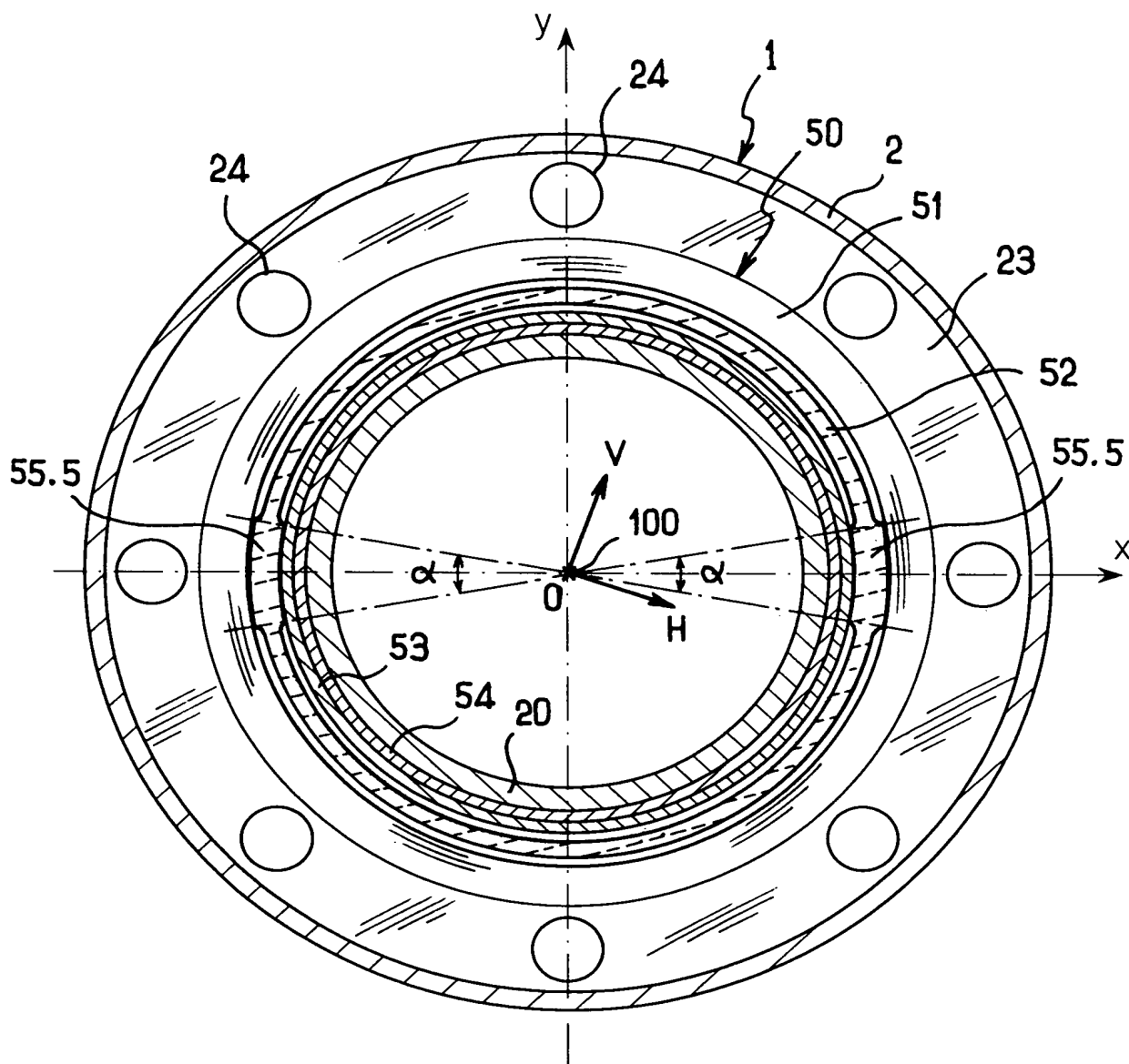




6 / 9

FIG. 6









[illegible]